



10/756,961

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	SHIBATA	Examiner:	PHAM
Serial No.:	10/756,961	Group Art Unit:	2814
Filed:	January 13, 2004	Docket No.:	10873.0871USD1
Title:	SEMICONDUCTOR MEMORY DEVICE WITH TRENCH-TYPE STACKED CELL CAPACITORS AND METHOD FOR MANUFACTURING THE SAME		

CERTIFICATE UNDER 37 CFR 1.10

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I hereby certify that the papers listed below are being deposited with the United States Postal Service Express Mail Post Office to Address service under 37 CFR 1.10 in an envelope addressed to: Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By

Name: Tracy Kimmel

AMENDMENT UNDER 37 C.F.R. 1.312

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 4 of this paper.